

**Government Polytechnic for Girls, Surat
EC & ICT Department**

Report on Workshop: “PCB Design Using KiCad” Under SSIP 2.0



Date: 6th May 2025 (Tuesday)

Time: 10:30 AM to 05:00 PM

Venue: Room 106B

The **EC & ICT Department** of Government Polytechnic for Girls, Surat successfully organized a one-day workshop on **“PCB Design Using KiCad”** under the **SSIP 2.0** initiative on **6th May 2025**. The workshop was specially designed for the **2nd semester students** of the EC and ICT departments to provide them with hands-on experience in PCB design using **KiCad**, an open-source electronic design automation tool.



Objective

The primary objective of this workshop was to:

- Familiarize students with PCB design software tools.
- Offer hands-on experience with KiCad for real-time PCB layout creation.
- Encourage innovation and technical skill development in line with SSIP 2.0 objectives.



The workshop included the following activities:

- **Introduction Session:** Opening remarks by the faculty members, introducing the objectives and schedule of the workshop.
- **Technical Session:** Detailed guidance on KiCad software usage, component placement, schematic creation, and PCB layout design.



- **Hands-on Practice:** Students actively participated in designing their PCBs under expert supervision.
- **Interactive Discussion:** Students engaged in Q&A sessions to clarify doubts and discuss design challenges

Outcome

The students gained:

- Practical exposure to PCB design tools.
- Enhanced confidence in applying theoretical knowledge to real-world projects.
- Insight into circuit design and prototyping with open-source software.

The session was conducted by **Mr. Manish Lad**, Lecturer in EC Department, Dr. S. & S. S. Gandhi College of Engineering & Technology, Surat who guided the students through various stages of PCB design, including schematic capture, layout, and simulation. His expert insights and practical demonstrations made the session highly interactive and informative.

The department extends sincere thanks to **Mr. Manish Lad** for his valuable contribution and to the principal for granting permission to conduct this workshop under SSIP 2.0.



Conclusion

The workshop concluded successfully with active participation from all attendees. The initiative significantly contributed to the students' technical growth and aligned with the vision of SSIP 2.0 to promote innovation and entrepreneurship at the academic level.