

## Phase 7 of SSIP 2.0 Scrutiny

**Event Name: Phase 7 of SSIP 2.0 Scrutiny**

**Date: 07/03/2026**

**Venue/ Mode: Upanishad Hall, Computer Engineering Department, LDCE**

**No of Participants: 73 students**

**Speakers Name : Mr. Anil Thakkar, Mr. Ratnam Shah, Mr. Anil Srivastava, Mr. Saurabh Chavda, Mr. Kathit Vasavada**

### **About Event:**

In alignment with the ongoing efforts to implement SSIP 2.0 of the Government of Gujarat at LDCE, initiatives were undertaken to ensure that a greater number of students benefit from the financial and developmental support provisions under the policy. As part of this process, fresh applications were invited from interested students for the development of Proof of Concept (PoC)/prototype of their innovative ideas.

The response from students has been highly encouraging. Following the directives of the Institute SSIP Implementation Committee, the first phase of preliminary scrutiny was conducted centrally, resulting in the shortlisting of applications. The shortlisted candidates have subsequently been instructed to present their ideas before a panel comprising one internal expert, one external expert, and members of the Institute SSIP Implementation Committee.

As part of the next stage, the presentation-cum-evaluation of the shortlisted PoCs was scheduled for 7th March 2026, from 11:00 AM to 2:00 PM.

### **Outcome of Event:**

16 projects were shortlisted and given financial assistance through SSIP 2.0.

### **Event Photos**

