

GOVERNMENT ENGINEERING COLLEGE-VALSAD ELECTRICAL ENGINEERING DEPARTMENT

Date: 24-02-2026 and 25-02-2026

SSIP Departmental Scrutiny Electrical Engineering Department February 2026

The SSIP Department Level Scrutiny was conducted successfully on 24/02/2026 & 25/02/2026 for Electrical Engineering Students, where Nine teams comprising a total of 27 students presented their innovative projects. Each team demonstrated their ideas, prototypes and implementation strategies before a panel of three committee members of the department - Dr. K. L. Mokariya, Prof. H. N. Zala, Prof. A. S. Mishal. The scrutiny aimed to assess the feasibility, innovation, and potential impact of the projects under the Student Start-up and Innovation Policy (SSIP). The participating students showcased their understanding of the problem statements, technical solutions, and possible applications, reflecting their commitment to innovation and entrepreneurship.

The committee members carefully evaluated each project based on key



parameters such as originality, technical feasibility, market potential, and societal impact. They provided constructive feedback, highlighting the strengths of each project while also suggesting improvements where necessary. Some projects

displayed strong innovation and practical applicability, while others required refinements in technical execution and market viability. The interaction between students and the panel was insightful, allowing participants to gain valuable suggestions for further development.

Overall, the scrutiny process was engaging and fruitful, identifying promising projects that could be further nurtured under SSIP. The committee emphasized the importance of continuous improvement and guided the teams on the next steps, including refining their concepts and preparing for higher-level evaluations. The event successfully encouraged students to think critically and work towards transforming their ideas into viable solutions, aligning with the objectives of the SSIP initiative.

Electrical
Dept. SSIP Coordinator

H.O.D.
Electrical Engg. Dept