

SSIP Awareness and Design Thinking Session for 1st year Computer Science Engineering Students

13th September, 2024

An awareness session for SSIP Policy and the introduction to Design Thinking for innovation, for first year students was conducted for Computer Engineering Department students on 13th September, at B-207, by Prof. Amit Rathod.

Key Features:

- **Organizing Department:** SSIP Cell, VGEC
- **Expert Details:** Prof. Amit Rathod
- **Date:** 13th September, 2024
- **Venue:** B 207
- **No. of Participants:** 66
- **Faculty Coordinators:** Prof. A. N. Dave

Learning Lessons/Discussion Points:

- Introduction to SSIP Policy
- Design Thinking for Innovation
- Opportunities provided under policy
- Different case studies/Examples

Glimpses:



