



**DR. S. & S. S. GHANDHY GOVERNMENT ENGINEERING  
COLLEGE**

**SURAT**

(Under Directorate of Technical Education, Government of Gujarat,  
Gandhinagar)



### **REPORT OF One Day Workshop On “PCB Design”**

Electronics and Communication Engineering Department under SSIP & ISTE Students’ Chapter of Dr. S. & S. S. Ghandhy Government Engineering College, Surat jointly organized a One Workshop on “**PCB Design**” on 25<sup>th</sup> October 2023 by **Prof. Manish Lad (Lecturer at Dr. S. & S. S. Ghandhy College of Engineering and Technology, Surat)**. This workshop is organized for 5<sup>th</sup> and 7<sup>th</sup> Semester students.

#### **Brief Summary of Workshop**

Prof. Manish Lad began by elucidating the fundamental principles of PCB design. He highlighted the significance of understanding the electrical properties of the components, signal integrity, and the overall functionality of the circuit.

Prof. Lad discussed common challenges faced in PCB design, such as thermal management, electromagnetic interference (EMI), and the importance of proper grounding. He provided practical solutions and design strategies to overcome these challenges, emphasizing the need for a holistic approach. An essential part of the presentation focused on the role of simulation tools in PCB design. Prof. Manish Lad stressed the importance of utilizing simulation software to model and validate designs before fabrication, reducing the risk of errors and ensuring optimal performance. The lecture underscored the collaborative nature of modern PCB design. Prof. Lad highlighted the importance of interdisciplinary collaboration between electronics engineers, mechanical engineers, and software developers to achieve seamless integration and functionality. Prof. Manish Lad encouraged active participation from the audience, fostering a dynamic and engaging discussion. Attendees had the opportunity to ask questions, seek clarification, and share their experiences related to PCB design.

In conclusion, Prof. Manish Lad's presentation on PCB design at provided valuable insights into the evolving landscape of electronic design. The lecture not only covered the theoretical aspects but also emphasized the practical challenges and innovative solutions in the field of PCB design. The event served as a platform for knowledge exchange and enrichment, benefiting students, faculty, and professionals in attendance.

## Event Photos



Samsung Quad Camera  
Dharmesh clicked



Samsung Quad Camera  
Dharmesh clicked



Samsung Quad Camera  
Dharmesh clicked



Samsung Quad Camera  
Dharmesh clicked



Samsung Quad Camera  
Dharmesh clicked



**Dr. S. & S. S. GHANDHY GOVERNMENT ENGINEERING  
COLLEGE, SURAT**



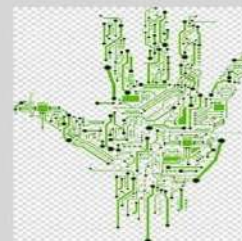
**ELECTRONICS AND COMMUNICATION ENGINEERING  
DEPARTMENT**

**SSIP**

Under  
**SSIP 2.0 & ISTE**

Jointly Organizes  
Workshop on

**"PCB Design"**



By

**Mr. Manish Lad,**

Lecturer,

**Dr. S. & S. S. Gandhi**

College of Eng. Technology Surat

**Date & Time**

**25/10/2023, Wednesday: 10:30 AM-05:30 PM**

**PG Lab (EC-204),**

**Electronics and Communication Engineering Department**

**Patron**

Dr. S. R. Joshi

Principal

DGGEC, Surat

**Convener**

Prof. T. P. Dave

HOD, Assoc. Prof. ECED

DGGEC, Surat

**Event Coordinators**

Prof. D. J. Patel

Asst. Prof. ECED

DGGEC, Surat