



B & B Institute of Technology

Vallabh Vidyanagar, Gujarat

BBIT SSIP Cell

&

Electrical Engineering Department, BBIT

Organizes a Seminar on

Intellectual Property Rights (IPR)

for Students of Diploma Engineering

23th September, 2023

11:30 AM to 1:30 PM

: Venue :

Computer Lab

Electrical Engineering Department

B & B Institute of Technology

Vallabh Vidyanagar, Gujarat

Coordinator:

Prof. Jayraj B. Solanki
Department Co-coordinator,
BBIT SSIP Cell

Registration Link:

<https://tinyurl.com/3yjpkpvy>

Last Date of Registration: 22nd September 2023

www.bbit.ac.in

