



B & B Institute of Technology

Vallabh Vidyanagar, Gujarat

BBIT SSIP Cell

&

Electrical Engineering Department

Organizes A Workshop on

**Sensors and PC based Data Acquisition
for Maintenance and Innovation
in Electrical Engineering**

for Students of Electrical Engineering

12th May, 202

10:30 AM to 1:30 PM

: Speaker :

Dr. Kalpeshkumar J. Chudasama

Associate Professor,

A. D. Patel Institute of Technology, New V. U. Nagar

Coordinator:

Prof. Jayraj B. Solanki

Department Coordinator,
BBIT SSIP Cell

Registration Link:

<https://tinyurl.com/w3d3wwy5>

Last Date of Registration: 11th May 2023

www.bbit.ac.in

